

L Number	Hits	Search Text	DB	Time stamp
-	2	20020032891.PN.	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 09:59
-	890	714/763.ccls.	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/02 10:25
-	34	(714/763.ccls.) and (non adj volatile adj memory)	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/02 10:25
-	16	(714/763.ccls.) and (non adj volatile adj memory) and (CPU or (central adj process\$3))	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/02 11:41
-	0	(714/763.ccls.) and (non adj volatile adj memory) and (CPU or (central adj process\$3)) and (partial adj stor\$3)	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/02 11:18
-	16	(non adj volatile adj memory) and (CPU or (central adj process\$3)) and (partial adj stor\$3)	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/02 11:04
-	214	(non adj volatile adj memory) and (CPU or (central adj process\$3)) and (part\$3 adj stor\$3)	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/02 11:08
-	2	6490195.pn.	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/02 11:08
-	0	(714/763.ccls.) and (non adj volatile adj memory) and (CPU or (central adj process\$3)) and (part\$3 adj stor\$3)	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/02 11:21
-	0	(714/718.ccls.) and (non adj volatile adj memory) and (CPU or (central adj process\$3)) and (part\$3 adj stor\$3)	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/02 11:22
-	29	(714/718.ccls.) and (non adj volatile adj memory) and (CPU or (central adj process\$3))	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/02 11:35
-	4	(714/718.ccls.) and (non adj volatile adj memory) and (CPU or (central adj process\$3)) and ECC	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/02 14:30
-	9	(714/763.ccls.) and (non adj volatile adj memory) and (CPU or (central adj process\$3)) and ECC	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/02 15:40
-	16	(365/200.ccls.) and (non adj volatile adj memory) and (CPU or (central adj process\$3)) and ECC	USPAT; US-PPGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/02 12:29

-	2	6078519.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/02 12:29
-	0	(714/763.ccls.) and (non adj volatile adj memory) and (CPU or (central adj process\$3)) and ECC and (semiconductor with chip with (non adj volatile adj memory) with CPU)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/02 16:06
-	0	(714/763.ccls.) and (semiconductor with chip with (non adj volatile adj memory) with CPU)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/02 16:07
-	20	(semiconductor with chip with (non adj volatile adj memory) with CPU)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/02 16:49
-	19	(714/763.ccls.) and (matrix with ECC)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/02 16:50
-	3	(714/763.ccls.) and (matrix with ECC with table)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/02 17:52
-	25	(mask adj ROM) with ECC	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:04
-	2	20020049944.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 09:46
-	1	(mask adj ROM) with ECC with generat\$3 with program	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 10:54
-	1	(mask adj ROM) with ECC with program	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 10:55
-	3	(mask adj ROM) with ECC with program\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:21
-	630	ECC with program\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:21
-	291	ECC with program\$4 with stor\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:21
-	134	ECC with program with stor\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:22

	57	ECC with program with storing	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 11:27
	0	storing adj ECC adj program	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 11:23
	1	storing adj (error adj correcting) adj program	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 11:25
	2	stor\$3 adj (error adj correcting) adj program	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 11:25
	65	(stor\$3 adj program) with (error adj correct\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 11:48
	0	(stor\$3 adj program) with (error adj correct\$3) with (erase adj prohibition)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 11:49
	0	(stor\$3 adj program) with (error adj correct\$3) with (erase)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 11:49
	0	(stor\$3 adj program) with (error adj correct\$3) with (non adj erase)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 11:49
	0	(stor\$3 adj program) with (error adj correct\$3) with (non adj eras\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 11:50
	0	(stor\$3 adj program) with (error adj correct\$3) with (eras\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 11:50
	0	(stor\$3 adj program) with (error adj correct\$3) with (eras\$3 or delet\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 11:51
	0	((stor\$3 adj program) with (error adj correct\$3)) same (eras\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 11:51
	0	((stor\$3 adj program) with (error adj correct\$3)) and (non adj eras\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 11:52
	1	((stor\$3 adj program) with (error adj correct\$3)) and (eras\$3 with prohibi\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 11:52

	22	((stor\$3 adj program) with (error adj correct\$3)) and (eras\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:58
	1	eras\$3 adj prohibition adj area	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:59
	69	eras\$3 adj prohibition	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 11:59
	41	(eras\$3 adj prohibition) and (error adj correction)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 12:21
	1	(eras\$3 adj prohibition adj area) and (error adj correction)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 12:07
	1	(eras\$3 adj prohibition adj (area or region or part or space)) and (error adj correction)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 12:22
	1	(eras\$3 adj prohibition adj (area or region or part or space)) and (error adj correct\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 12:23
	1	(eras\$3 adj prohibition adj (area or region or part or space))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 12:24
	1	(eras\$3 adj prohibit\$3 adj (area or region or part or space))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 12:24
	18	(eras\$3 adj prohibit\$3) with (area or region or part or space)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 14:47
	1	((eras\$3 adj prohibit\$3) with (area or region or part or space)) and (rewrit\$3 with allowable)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 12:27
	1	((eras\$3 adj prohibit\$3) with (area or region or part or space)) and (writ\$3 with allowable)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 12:27
	7	((eras\$3 adj prohibit\$3) with (area or region or part or space)) and (writ\$3 with allow\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 12:28
	1	(6546517.pn.) and RAM and CPU	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/03 15:16

	6	RAM with CPU with FLASH with reset with program	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 16:08
	0	RAM with CPU with FLASH with reset with (transfer\$4 adj program\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 16:16
	0	RAM with FLASH with reset with (transfer\$4 adj program\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 16:09
	0	RAM with (non adj volatile adj memory) with reset with (transfer\$4 adj program\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 16:10
	2	RAM with CPU with FLASH with reset with (transfer\$4 with program\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 16:16
	0	(6546517.pn.) and flash and erasable	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 09:59
	0	(6546517.pn.) and flash and EEPROM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 10:00
	2	(6546517.pn.) and flash	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 10:31
	79	(ECC with generat\$2) with (exclusive adj "or")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 12:23
	8	(stor\$3 with (("same" or one) adj data) with (different adj address\$2) with memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 12:26
	1	(stor\$3 with (("same" or one) adj data) with (different adj address\$2) with memory) and (error adj correct\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 12:26
	1	((stor\$3 or writ\$3) with (("same" or one) adj data) with (different adj address\$2) with memory) and CPU and (read\$3 with data with "AND")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 12:28
	1	((stor\$3 or writ\$3) with (("same" or one) adj data) with (different adj address\$2) with memory) and CPU and (read\$3 with data with "OR")	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 12:28
	3	(stor\$3 with (("same" or one) adj data) with (different adj address\$2) with memory) and CPU	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 12:32

-	1	(stor\$3 with ("same" or one) adj data) with (different adj address\$2) with memory) and (non adj volatile adj memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 12:32
-	2	((stor\$3 or writ\$3) with ("same" or one) adj data) with (different adj address\$2) with memory) and (non adj volatile adj memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 14:33
-	2	5097445.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 15:42
-	2	5097445.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 16:41
-	2	6000021.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 16:42
-	2	5097445.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 16:44
-	2	6546517.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 16:47
-	4	(flash adj memory) with voltage with threshold with high with low with levels	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/04 16:48